

WHAT IS CLAIMED IS:

1. A method of using a first data processor to manage resources of a second data processor which performs data processing functions that support user applications,
5 comprising:

the first data processor making a remote procedure call to the second data processor to invoke on the second data processor a program that supports management of data processing resources of the second data processor; and

10 the second data processor executing the program in response to the remote procedure call.

2. The method of Claim 1, wherein said step of making a remote procedure call includes the first data processor providing a remote procedure call command to the second data processor, and wherein said executing step includes the second data processor decoding the remote procedure call command and calling the program.
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3. The method of Claim 1, wherein said executing step includes the program permitting the first data processor to write to a memory space associated with the second data processor.

4. The method of Claim 3, including the first data processor downloading a further program to said memory space in conjunction with operation of the first-mentioned program.

5 5. The method of Claim 1, wherein said executing step includes the program providing information indicative of a capability of the second data processor.

10 6. The method of Claim 5, wherein said capability information includes information indicative of a native character size of the second data processor.

7. The method of Claim 5, wherein said capability information includes information indicative of an operating system of the second data processor.

15 8. The method of Claim 5, wherein said capability information includes information that identifies the second data processor.

9. The method of Claim 1, wherein said executing step includes the program activating a desired communication protocol for communication between the first and second data processors.

10. The method of Claim 1, wherein said executing step includes the program changing runtime priority associated with a data processing function of the second data processor.

5 11. The method of Claim 10, wherein said changing step includes one of pausing and resuming said data processing function.

10 12. The method of Claim 1, wherein said executing step includes the program permitting the first data processor to read from a memory space associated with the second data processor.

15 13. The method of Claim 1, wherein said step of making a remote procedure call includes the first data processor providing to the second data processor a remote procedure call command which includes an address that points to a location of the program in a memory space of the second data processor.

14. The method of Claim 1, including providing the program in a memory space of the second data processor.

20 15. The method of Claim 14, wherein said providing step includes making a remote procedure call to the second data processor to invoke on the second data processor

a further program and, in response to the remote procedure call, the second data processor executing the further program to download the first-mentioned program into the memory space of the second data processor.

5 16. A data processing apparatus, comprising:

 a data processor for performing data processing functions that support user applications;

 a memory coupled to said data processor, said memory including a program that supports management of data processing resources of said data processor; and

10 said data processor having an input for receiving from another data processing apparatus a remote procedure call, said data processor responsive to said remote procedure call for executing said program.

15 17. The apparatus of Claim 16, provided as a single integrated circuit chip.

18. The apparatus of Claim 17, wherein said integrated circuit chip is one of a microprocessor chip and a digital signal processor chip.

19. The apparatus of Claim 16, wherein said program permits said another
20 data processing apparatus to write to a memory space associated with said data processor.

20. The apparatus of Claim 16, wherein said program provides information indicative of a capability of the data processor.

21. The apparatus of Claim 20, wherein said capability information includes
5 information indicative of a native character size of the data processor.

22. The apparatus of Claim 20, wherein said capability information includes information indicative of an operating system of said data processor.

10 23. The apparatus of Claim 20, wherein said capability information includes information that identifies said data processor.

15 24. The apparatus of Claim 16, wherein said program activates a desired communication protocol for communication between said data processing apparatus and said another data processing apparatus.

25. The apparatus of Claim 16, wherein said program changes a runtime priority of one of said data processing functions on said data processor.

20 26. The apparatus of Claim 16, wherein said program permits said another data processing apparatus to read from said memory.

27. A data processing system, comprising:

a first data processor for performing a first data processing function;

a second data processor for performing second data processing functions that

5 support user applications;

a communication path coupled to said first and second data processors for permitting communication therebetween;

a memory associated with said second data processor, said memory including a program that supports management of data processing resources on said second data

10 processor; and

said first data processor operable for making a remote procedure call to said second data processor to invoke said program on said second data processor, said second data processor responsive to said remote procedure call for executing said program.

15 28. The system of Claim 27, provided as a single integrated circuit chip.

29. The system of Claim 28, including a man/machine interface coupled to said first data processor for permitting communication between said first data processor and a user.

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30. The system of Claim 27, wherein said first data processor is one of a microprocessor and a digital signal processor, and said second data processor is one of a microprocessor and a digital signal processor.

5 31. The system of Claim 27, including a man/machine interface coupled to said first data processor for permitting communication between said first data processor and a user.

10 32. The system of Claim 31, wherein said man/machine interface includes one of a tactile interface and a visual interface.

15 33. The system of Claim 27, wherein said communication path extends through a data network.

34. The system of Claim 33, wherein said data network is the Internet.

35. The system of Claim 27, wherein said first and second data processors are located remotely from one another.

36. The system of Claim 35, including a man/machine interface coupled to said first data processor for permitting communication between said first data processor and a user.

5 37. The system of Claim 27, wherein said program permits said first data processor to read from a memory space associated with said second data processor.

10 38. The system of Claim 27, wherein said program provides information indicative of a capability of the second data processor.

10 39. The system of Claim 38, wherein said capability information includes information indicative of a native character size of the second data processor.

15 40. The system of Claim 38, wherein said capability information includes information that identifies said second data processor.

41. The system of Claim 38, wherein said capability information includes information indicative of an operating system of said second data processor.

42. The system of Claim 27, wherein said program activates a desired communication protocol for communication between said first and second data processors.

5 43. The system of Claim 27, wherein said program changes a runtime priority of one of said second data processing functions on said second data processor.

44. The system of Claim 27, wherein said program permits said first data processor to write to a memory space associated with said second data processor.

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45. The system of Claim 44, wherein said first data processor is cooperable with said program for downloading a further program to said memory space.